Overview

The Digilent FX12 board (the FX12) is an integrated circuit development platform for Xilinx’s Virtex-4 FX12 FPGA. This document describes modifications to the FX12 board that are made after the original manufacturing operation.

Item #1

Description

The two JTAG data signals (TDI and TDO) routed to the parallel-4 connector on the FX12 board were swapped, rendering the parallel-4 connector unusable. These signals are not swapped for the parallel-3 6-pin connector; this problem only affects the parallel-4 connector.

This item affects only Rev B boards.

Remedy

Boards have been modified by Digilent to correct this problem. The TDI and TDO traces between the parallel-3 and parallel-4 connectors were cut, and jumpers added to connect the traces correctly. This modification has been validated and tested on all boards. No functional problems exist.

This fix will be incorporated into the PCB for Rev C boards.

Requirements

No actions are required by users. Required modifications are made by Digilent prior to distribution.