For more information on the parts used in this design, please refer to:
www.analog.com/AD5628 - 8-channel, 12-bit SPI Voltage Output denseDAC with 5 ppm/degree-C On-Chip Reference

Note: The voltage applied to VCC must be kept between 2.7V and 5.5V in order to avoid damaging the parts used in this circuit.

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